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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:) Group Art Unit: 2826
NAMATAME et al.)
Serial No. 09/847,163)
Filed: May 1, 2001)
For: SEMICONDUCTOR DEVICES AND METHODS)
FOR MANUFACTURING THE SAME)

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

Assistant Commissioner for Patents
Washington, DC 20231

Dear Sirs:

In response to the Election/Restriction Requirement dated March 26, 2002, the response being due by Tuesday, May 28, 2002 by the enclosed petition for extension of time (the Patent Office was closed on Monday, May 27), please enter and consider the following.

Applicant hereby elects to prosecute Group II, claims 6-16, drawn to methods of making a semiconductor device.

Examination on the merits is respectfully requested. If any additional fees are necessary for consideration of this paper, the Commissioner is hereby authorized to charge payment of such fees to Deposit Account No. 50-0585. A copy of this paper is enclosed.

Respectfully submitted,

Alan S. Raynes
Reg. No. 39,809
KONRAD RAYNES & VICTOR LLP
315 South Beverly Drive, Suite 210
Beverly Hills, CA 90212
Customer No. 24033

Dated: May 28, 2002

(310) 556-7983 (tele. general)
(310) 871-8448 (tele. direct)
(310) 556-7984 (facsimile)

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on May 28, 2002.

Alan S. Raynes May 28, 2002
Alan S. Raynes (Date)